SYSTEM CONFIGURATION GENERATE T5377

CONFIGURATION OF TEST HEAD

PIN CONFIGURATION 1. 512DR+ 320I/O(QUARTER)

2. 1024DR+ 640I/O(HALF)

3. 2048DR+ 1280I/O(FULL) [1-3] .....>3

NUMBER OF TEST HEAD [1, 2] .....>2

TYPE OF DR PE [1:DR PE, 2:24Ch HVDR PE] .....>1

CONFIGURATION OF TEST HEAD 1

PE BOARD 0. NOT EXIST

1. EXIST

DR PIN	1A1	1A5	17A1	25A1
CHILD A, B ....>	1	1	1	1
CHILD C, D ....>	1	1	1	1
CHILD E, F ....>	1	1	1	1
CHILD G, H ....>	1	1	1	1
IO PIN	33A1	97A1	33A5	97A5
CHILD A, B ....>	1	1	1	1
CHILD C, D ....>	1	1	1	1
CHILD E, F ....>	1	1	1	1
CHILD G, H ....>	1	1	1	1

CONFIGURATION OF TEST HEAD 2

PE BOARD 0. NOT EXIST

1. EXIST

DR PIN	1A1	1A5	17A1	25A1
CHILD A, B ....>	1	1	1	1
CHILD C, D ....>	1	1	1	1
CHILD E, F ....>	1	1	1	1
CHILD G, H ....>	1	1	1	1
IO PIN	33A1	97A1	33A5	97A5
CHILD A, B ....>	1	1	1	1
CHILD C, D ....>	1	1	1	1
CHILD E, F ....>	1	1	1	1
CHILD G, H ....>	1	1	1	1

CONFIGURATION OF DPU

TEST HEAD 1	DC	CONFIGURATION [1-64] ……> 1-64
	PPS	CONFIGURATION [1-64] ……> 1-256
TEST HEAD 2	DC	CONFIGURATION [1-64] ……> 1-64
	PPS	CONFIGURATION [1-64] ……> 1-256

AC FREQUENCY (Hz) [50, 60] ……> 60

CONFIGURATION OF FTU

FLASH OPTION [Y, N] ……> NO

CONFIGURATION OF FM

NUMBER OF FMRA BOARD [0-16] ……> 16

SIZE OF FMRA BOARD [1:8G, 2:16G] ……> 1

PM (PATTERN MEMORY) BOARD EXIST [Y, N] ……> NO

CONFIGURATION OF MRA

MRA4 EXIST [Y, N] ……> YES

RCPU STN1 A-D EXIST [Y, N] ……> YES

RCPU STN1 E-H EXIST [Y, N] ……> YES

RCPU STN2 A-D EXIST [Y, N] ……> YES

RCPU STN2 E-H EXIST [Y, N] ……> YES

END SAVE